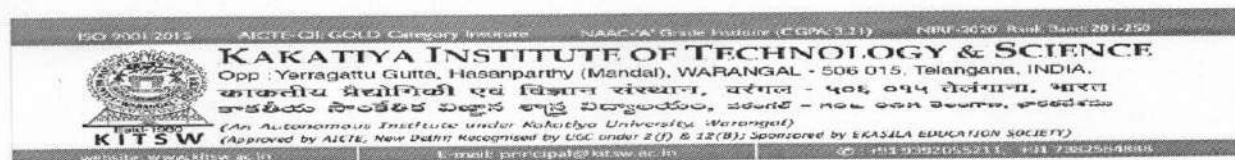


REMEDIAL CLASS TIMETABLE (for students who have backlogs)



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

No. 1654/ECE/KITSW/2025

Date: 23.10.2025

CIRCULAR

Subject: Conduct of Remedial Classes for Students with Backlogs

As part of our efforts to support students in clearing their backlogs, remedial classes have been scheduled as per the timetable below. These sessions are intended to provide additional guidance and strengthen understanding of key topics. Both faculty members and students with backlogs are requested to note the following:

For Faculty Members:

- Conduct classes as per the schedule provided.
- Cover important topics that will help students in exam preparation.
- Update CourseWeb for the corresponding course with:
 - Question papers and solutions of at least two previous ESEs
 - Twelve tutorial sheets covering important topics.
- Submission of attendance statement immediately the conduction of scheduled class

For Students:

- Attend the sessions to take advantage of the support being provided.
- Utilize the materials and explanations shared by the faculty.
- Focus on the topics you find most useful for your preparation.
- Treat these sessions as an opportunity to build confidence before exams.

The remedial classes are being arranged with the aim of assisting students in their preparation, and the active involvement of faculty in delivering them will ensure their effectiveness.

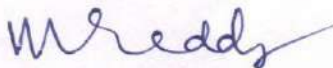
Schedule for Remedial Classes

Venue: B-VI 101

S.No.	SEME STER	COURSE NAME	DEPAR TMENT	NO OF BACKLOG STUDENTS	Date	Time	Faculty handling
1.	III	Analog Circuits - I	ECE	46	28.10.25	16:00-16:45	V. Shobhan Reddy
2.	III	Signals & Systems	ECE	57	28.10.25	16:45-17:30	Dr. K. Sowjanya
3.	III	Switching Theory & Logic Design	ECE	60	29.10.25	16:00-16:45	P. Yugander
4.	IV	Analog Circuits-II	ECE	53	29.10.25	16:45-17:30	Dr. M. Raju
5.	IV	Electromagnetic Waves & Transmission Lines (B VI 203)	ECE	53	30.10.25	11:45-12:30	Dr. M. Chandrasekhar
6.	IV	Digital Design	ECE	47	30.10.25	16:00-16:45	Ch. Pavan
7.	III	Object Oriented Programming	CSE	43	30.10.25	16:45-17:30	Dr. T. Ranjith
8.	IV	Probability & Random Processes	ECE	62	31.10.25	16:00-16:45	Dr. R. Srikanth

REMEDIAL CLASS TIMETABLE (for students who have backlogs)

9.	IV	Pulse And Digital Circuits	ECE	62	31.10.25	16:45-17:30	Dr. Md. Abdul Muqeem
10.	VI	Advanced Data Structures	CSE	23	01.11.25	11:30-12:15	Smt. R. Radhika
11.	V	Artificial Intelligence And Machine Learning With Python	ECE	17	01.11.25	12:15-01:00	Dr. G. Raghotham Reddy
12.	V	Antennas And Wave Propagation	ECE	14	01.11.25	01:00-01:45	Sri B. Komuraiah
13.	V	Communication Systems	ECE	21	03.11.25	16:00-16:45	Dr. S. P. Girija
14.	V	Microprocessors And Microcontrollers	ECE	28	03.11.25	16:45-17:30	Dr. J. Tarun Kumar
15.	V	Linear Integrated Circuits And Applications	ECE	30	04.11.25	16:00-16:45	Sri E. Suresh
16.	VI	Digital Signal Processing And Applications	ECE	41	04.11.25	16:45-17:30	Dr. D. Venu
17.	VI	Embedded Systems With Arm Processor And Applications	ECE	24	06.11.25	16:00-16:45	Sri D. Santosh Kumar
18.	VI	Industrial Internet Of Things	ECE	15	06.11.25	16:45-17:30	Dr. B. Ramadevi
19.	VI	VLSI Circuits And Systems	ECE	43	07.11.25	16:00-16:45	Dr. V. Raju
20.	III	Engineering Mathematics - III	MHS	78	07.11.25	16:45-17:30	Smt. K. Nirmala
21.	III	Network Analysis	EEE	58	08.11.25	11:30-12:15	Sri T. Praveen Kumar
22.	V	Linear Control Systems	EEE	44	08.11.25	12:15-01:00	Sri T. Praveen Kumar
23.	IV	Applicable Mathematics	MHS	62	08.11.25	01:00-01:45	Smt. K. Nirmala



**Dr. V. Venkateshwar Reddy,
Head, ECED**

Copy: 1. Dean AA
2. HoD, CSE
3. HoD, EEE
4. HoD, M&H